FRAME AND A.F. OUTPUT PENTODE

Pentode intended for use as frame output tube in television receivers and as A.F. power amplifier.

QUICK REFERENCE DATA				
Anode peak voltage	v_{a_p}	max.	2	kV
Cathode current	I_k	max.	100	mA
Output power	W_{o}		5.3	W

HEATING: Indirect by A.C. or D.C.; parallel supply

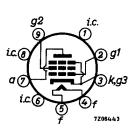
Heater voltage

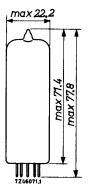
Heater current

v_f	6.3	v
$\overline{I_f}$	760	mA

DIMENSIONS AND CONNECTIONS

Base: Noval





Dimensions in mm

CAPACITANCES

Anode to all except grid No.1	$C_{a(g_1)}$		6.8	pF
Grid No.1 to all except anode	$c_{g_1(a)}$		13	pF
Anode to grid No.1	c_{ag}_1	max.	0.6	pF
Grid No.1 to heater	C_{g_1f}	max.	0.25	pF

OPTIMUM PEAK ANODE CURRENT IN FRAME OUTPUT OPERATION

The circuit should be designed so that the peak anode current does not exceed:

145 mA at
$$V_a$$
 = 60 V, V_{g_2} = 170 V, V_f = 6.3 V
190 mA at V_a = 70 V, V_{g_2} = 200 V, V_f = 6.3 V
220 mA at V_a = 80 V, V_{g_2} = 220 V, V_f = 6.3 V

The minimum available value of the peak anode current at end of life and V_f = 5.7 V is:

(Measured with Vi. constant)

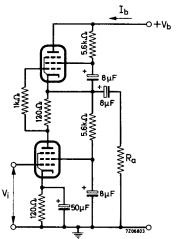
125 mA at
$$V_a$$
 = 60 V, V_{g_2} = 170 V
160 mA at V_a = 70 V, V_{g_2} = 200 V
185 mA at V_a = 80 V, V_{g_2} = 220 V

OPERATING CHARACTERISTICS A. F. nower amplifier, class A

A.F. power amplifier, class A (Measured with V_k constant)				
v_b		200		V
$^{ m R}{}_{ m g_2}$		470		Ω
$R_{\mathbf{k}}$		215		Ω
R_{a}		2.5		$k\Omega$
v_{i}	0	0.52	7.0	v_{RMS}
$^{ m I}{}_{ m a}$	65	-	64	mA
$I_{\mathbf{g_2}}$	3.2	-	11.4	mA
w _o	0	0.05	5.3	W
d_{tot}	-		10	%
two tubes in push	ı-pull			
v_{ba}		2 50		v
$v_{ m bg_2}$		200		V
$R_{\mathbf{k}}$		150		Ω
R_{aa} \sim		5.5		$k\Omega$
v_i	0	0.37	13.0	v_{RMS}
I_a	2x50		2x55	mA
${f I_{g_2}}$	2x2.0	_	2x13	mA
w_{o}	0	0.05	18.5	W
d_{tot}	-	-	4.5	%
	$\begin{array}{c} v_b \\ & R_{g_2} \\ & R_k \\ & R_a \\ & V_i \\ & I_a \\ & I_{g_2} \\ & W_o \\ & d_{tot} \\ \\ & V_{ba} \\ & V_{bg_2} \\ & R_k \\ & R_{aa} \\ & V_i \\ & I_a \\ & I_{g_2} \\ & W_o \\ \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

OPERATING CHARACTERISTICS (continued)

A.F. power amplifier, single ended push-pull



a) Single tone input signal

Supply voltage	v_b		300		V
Load resistance	$R_{a\sim}$		1		$k\Omega$
Grid No.1 driving voltage	v_i	0	0.41	5.4	v_{RMS}
Supply current	I_b	66	-	64	mA
Output power	$W_{\mathbf{o}}$	0	0.05	4.5	W
Distortion	d_{tot}	-	_	9.3	%
b) Double tone input signal					
Supply voltage	v_b		300		v
Load resistance	$R_{a_{\sim}}$		1		kΩ
Grid No.1 driving voltage	v_{i}	0		2.7	V_{RMS}^{1})
Supply current	I_b	66		64	mA
Output power	w_o	0		5.5	w
Distortion	d_{tot}	-		8.5	%

¹⁾ Value of each tone separately.

REMARK

Single tone data are obtained with a pure sinusoidal input voltage. However such an input voltage is in general not representative for the reproduction of music and speech, since a purely sinusoidal tone seldom occurs.

The double tone data are obtained with two sinusoidal signals of different frequencies but of the same amplitude. This appears to be far better in agreement with practice. In the case of full drive with two sinusoidal signals different in frequency but having the same amplitude, the output power is half the value obtained at full drive with a single sinusoidal input voltage of twice this amplitude. To make comparison possible the obtained output power with double tone has therefore been multiplied by 2.

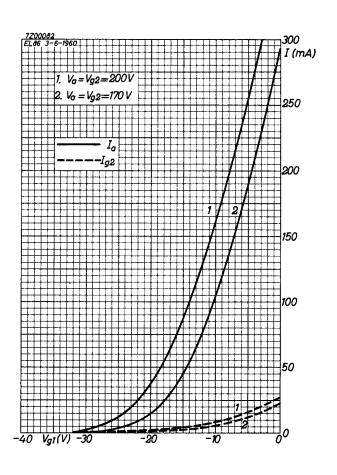
LIMITING VALUES (Design centre rating system)

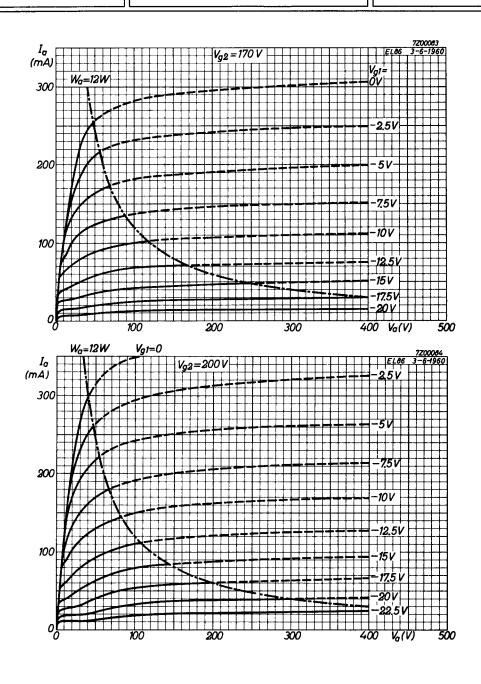
Anode voltage	$v_{a_{o}}$	max.	550	v
	v_a	max.	250	V
Anode peak voltage	v_{a_p}	max.	2	kV ¹)
Grid No.2 voltage	$v_{g_{2o}}$	max.	550	V
	v_{g_2}	max.	250	v
Anode dissipation	W_a	max.	12	W^2)
Grid No.2 dissipation:				
average	w_{g_2}	max.	1.75	W
peak	$w_{g_{2p}}$	max.	6	W
Cathode current	I_k	max.	100	mA
Grid No.1 resistor:				
automatic bias	R_{g_1}	max.	1	$M\Omega$
frame output application	_			
with automatic bias	R_{g_1}	max.	2	$M\Omega$
Cathode to heater voltage	$v_{\mathbf{kf}}$	max.	200	V

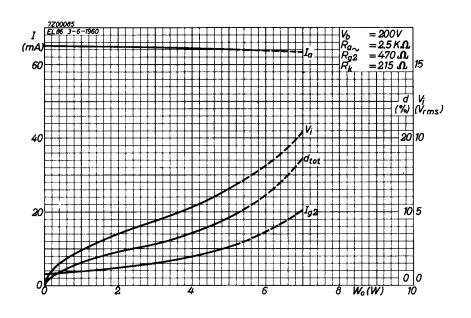
¹⁾ Valid for application in frame output circuits where the max. pulse duration is 4% of a cycle with a max. of 0.8 ms.

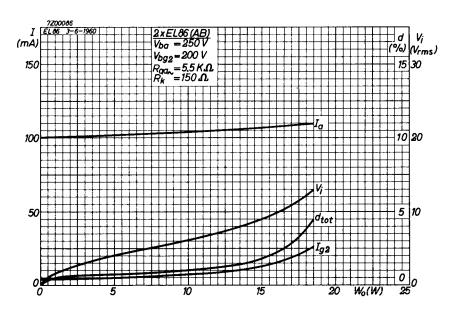
 $^{^{2}}$) For frame output application W_{a} = max. 10 W.

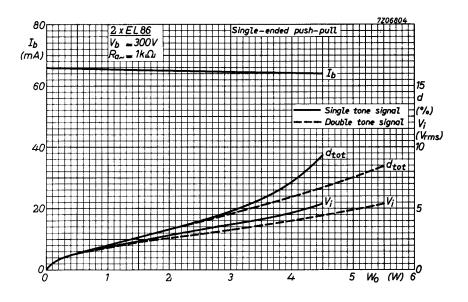
5

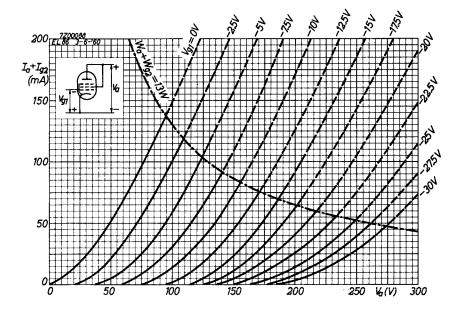


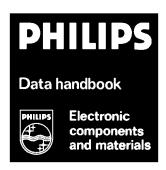












EL86

page	sheet	date
1	1	1969.01
2	2	1969.01
3	3	1969.01
4	4	1969.01
5	5	1969.01
6	6	1969.01
7	7	1969.01
8	8	1969.01
9	FP	1999.03.19